

Amendments to the Specification:

Please replace the paragraph beginning at page 2, line 26 with the following amended paragraph:

~~FIGURE 2 is~~ FIGURES 2A and 2B are a block diagram of a Voice-over-Internet Protocol processor including integrated USB ports, media access controllers, repeater ports and a pair of integrated pulse code modulation ports in accordance with the present invention.

Please replace the paragraph beginning at page 3, line 19 with the following amended paragraph:

~~FIGURE 2 is~~ FIGURES 2A and 2B are a block diagram of the network processor 12, the details of which are more fully described in the specification titled "Harrier-VT" as attached as Appendix A. Although the presently preferred embodiment is constructed around the TRICORE and Harrier processors available from Infineon Technologies of San Jose, California, it will be appreciated that any computing architecture having the functionality described could be substituted.